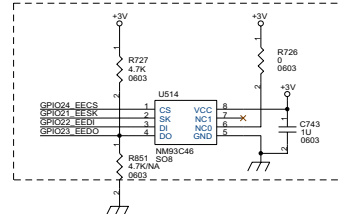
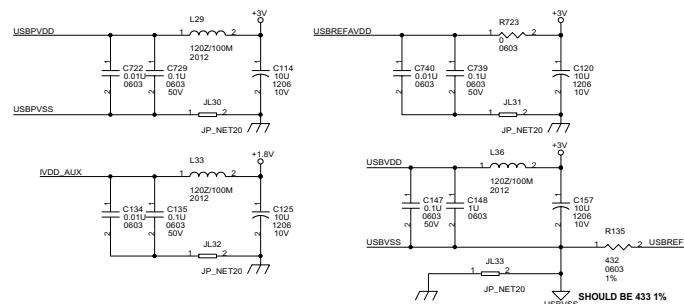
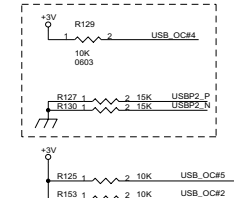
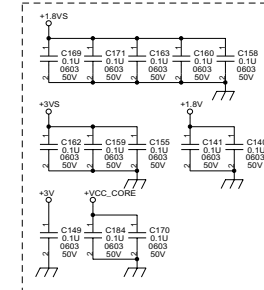
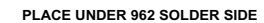
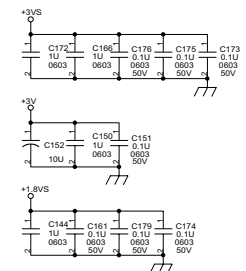
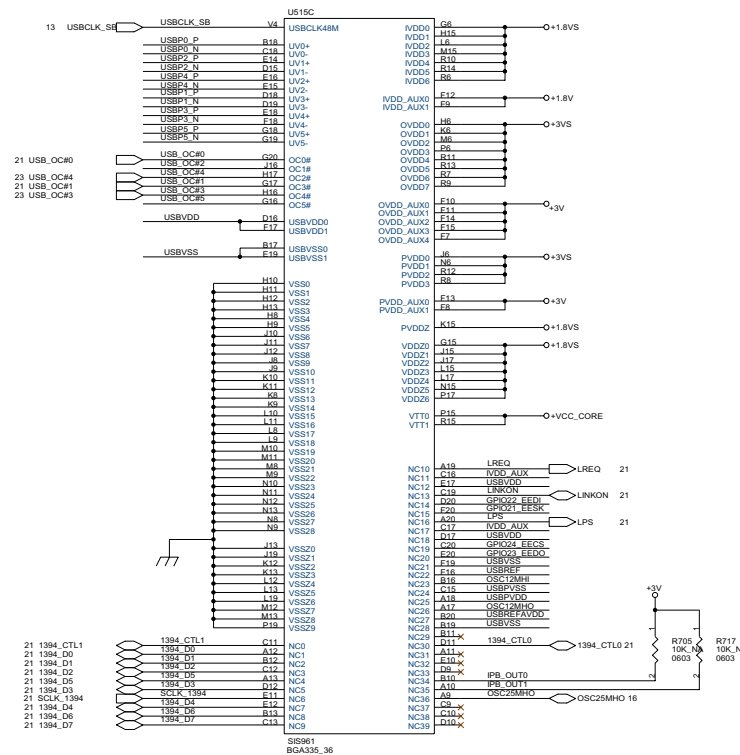
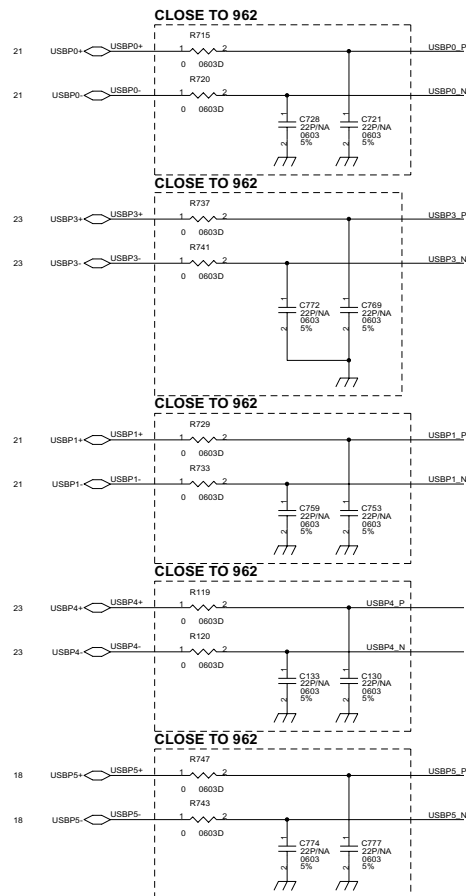
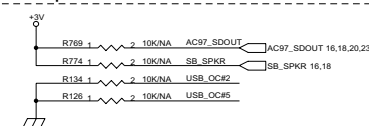
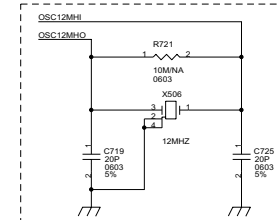


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FOR USB 2.0 USED



	Symbol	0	1	Default	Notes
	SB_SPKR (LPC addr mapping)	disable	enable	0	Internal Pull-down
	AC97_SDOUT (frap mode)	ROM	Reserved	0	Internal Pull-down
	USB_OC4# (South bridge debug mode)	enable	disable	1	
	IPB_OUT0(MuTIOL clock PLL)	enable	disable	0	Internal Pull-down
	IPB_OUT1(MuTIOL operation mode select)	enable	Partial-swing	0	Internal Pull-down
	AC97_SYNC (PCICLK PLL)	Full-swing	disable	0	Internal Pull-down



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