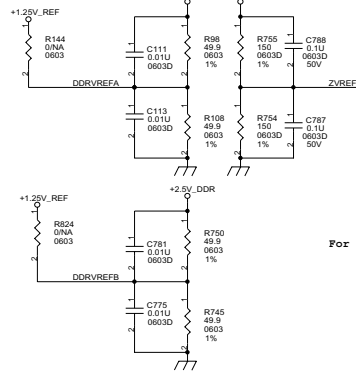
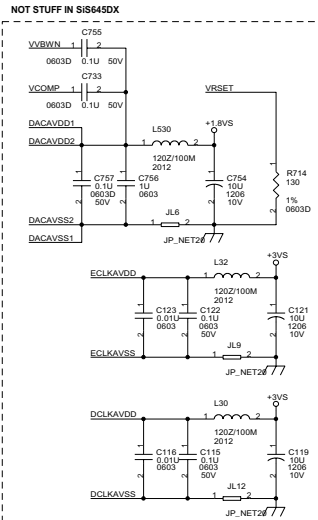
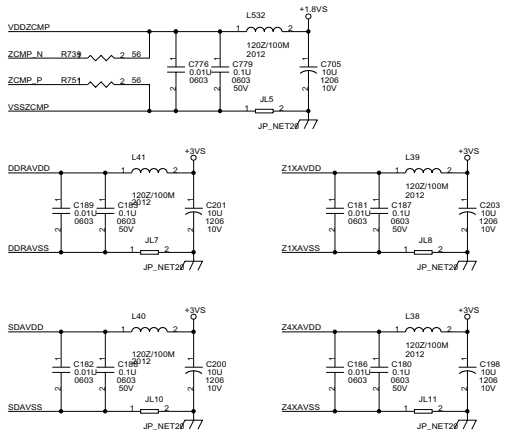
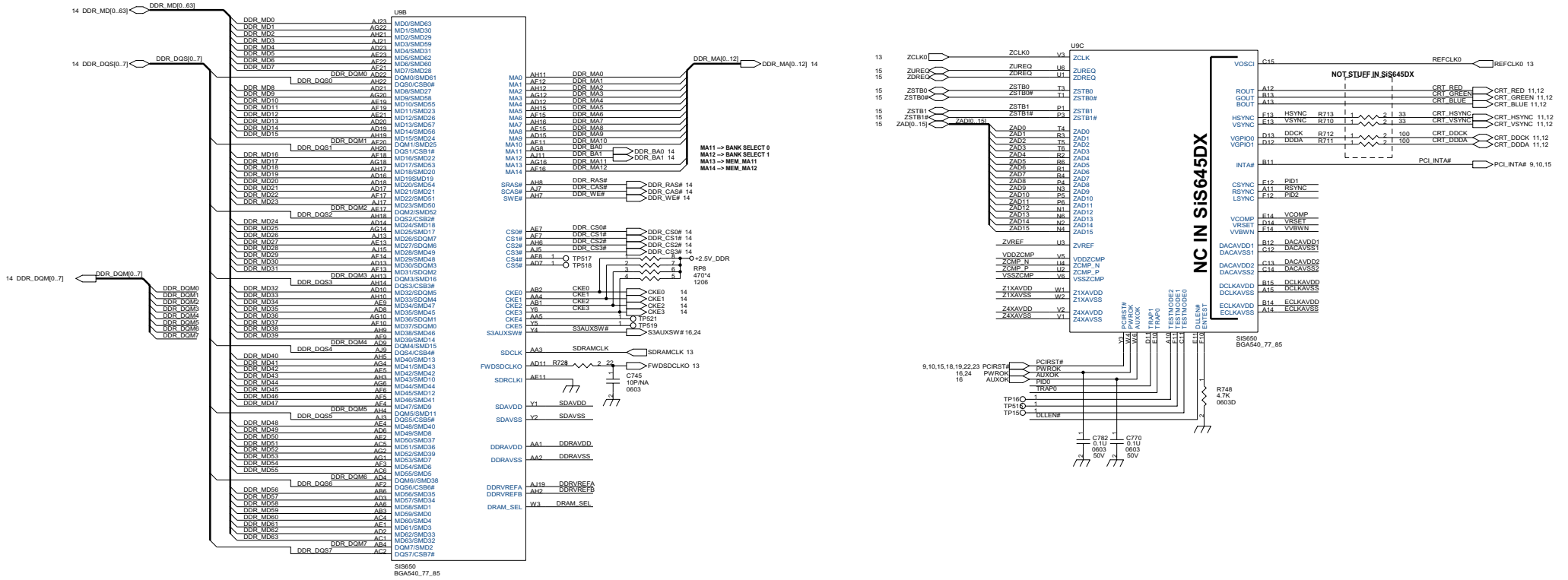
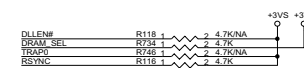


SIS M650/645DX(2/3)



	0	1	Default	Embedded pull-low (30~50K Ohm)
DLEN#	Enable PLL	Disable PLL	0	Yes
DRAM_SEL				
TRAP0	Disable Debug Mode	Enable Debug Mode	0	Yes
RSYNC	Disable VGA Int function	Enable VGA Int function		
CSYNC			1	
TRAP1	Reserved for Panel_ID	Reserved for Panel_ID		
LSYNC	Reserved for Panel_ID	Reserved for Panel_ID		



	For M650
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Title	SIS M650/M645DX (2/3)
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