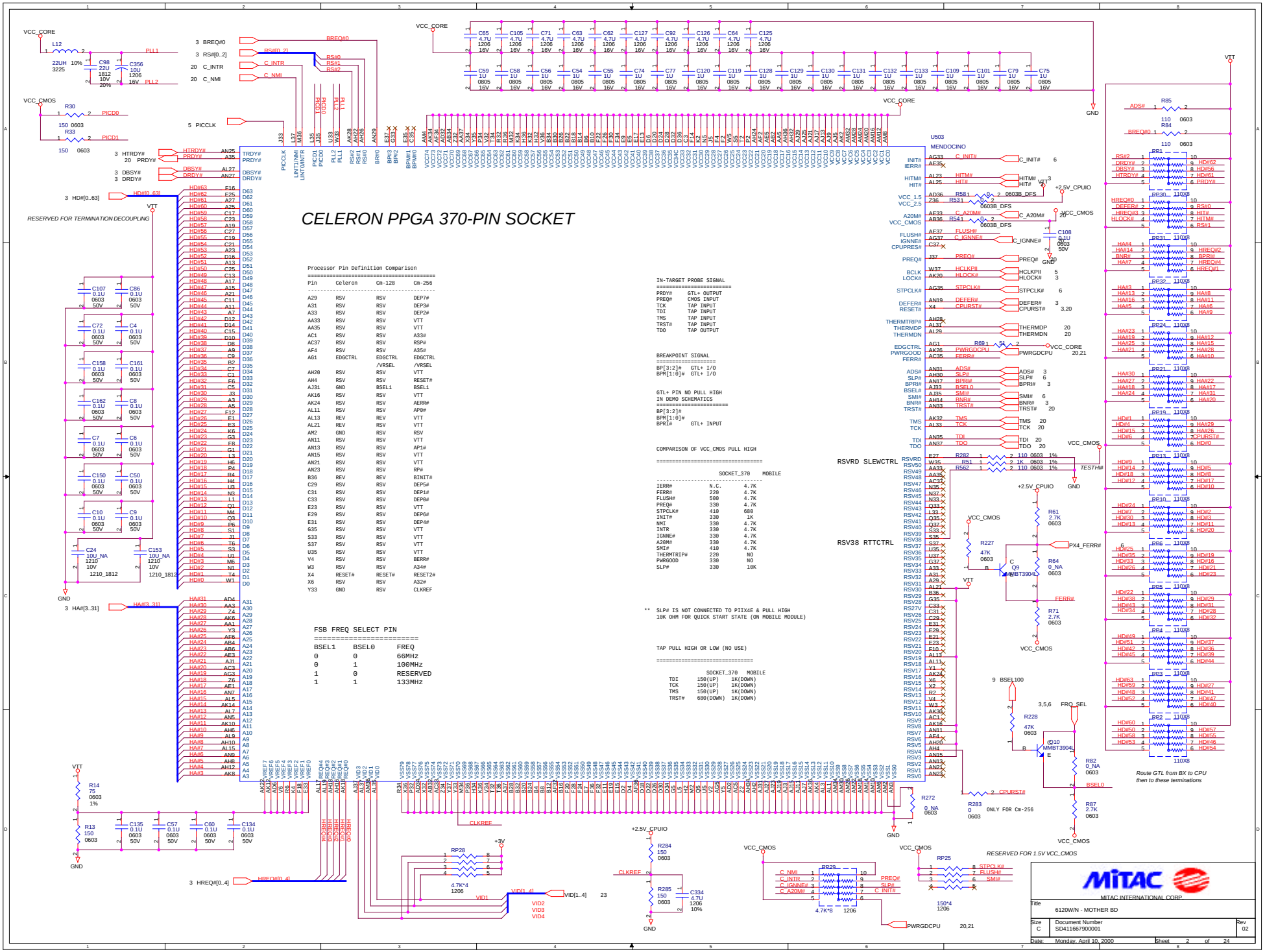
CELERON PPGA 370-PIN SOCKET
Processor Pin Definition Comparison
Pin Celeron Cm-128 Cm-256
A29 RSV RSV DEP7#
A31 RSV RSV DEP3#
A33 RSV RSV DEP2#
AA33 RSV RSV VTT
AA35 RSV RSV VTT
AC1 RSV RSV A33#
AC37 RSV RSV RSP#
AF4 RSV RSV A35#
AG1 EDGCTRL EDGCTRL EDGCTRL
/VRSEL /VRSEL
AH20 RSV RSV VTT
AH4 RSV RSV RESET#
AJ31 GND RSV BSEL1
AK16 RSV RSV VTT
AK24 RSV RSV AERR#
AL11 RSV RSV AP0#
AL13 REV REV VTT
AL21 REV RSV VTT
AM2 GND RSV RSV
AN11 RSV RSV VTT
AN13 RSV RSV AP1#
AN15 RSV RSV VTT
AN21 RSV RSV VTT
AN23 RSV RSV RP#
B36 REV REV BINIT#
C29 RSV RSV DEP5#
C31 RSV RSV DEP1#
C33 RSV RSV DEP0#
E23 RSV RSV VTT
E29 RSV RSV DEP6#
E31 RSV RSV DEP4#
G35 RSV RSV VTT
S33 RSV RSV VTT
S37 RSV RSV VTT
U35 RSV RSV VTT
V4 RSV RSV BERR#
W3 RSV RSV A34#
X4 RESET# RESET# RESET2#
X6 RSV RSV A32#
Y33 GND RSV CLKREF
IN-TARGET PROBE SIGNAL
PRDY# GTL+ OUTPUT
PREQ# CMOS INPUT
TCK TAP INPUT
TDI TAP INPUT
TMS TAP INPUT
TRST# TAP INPUT
TDO TAP OUTPUT
BREAKPOINT SIGNAL
BP[3:2]# GTL+ I/O
BPM[1:0]# GTL+ I/O
GTL+ PIN NO PULL HIGH
IN DEMO SCHEMATICS
BP[3:2]#
BPM[1:0]#
BPRI# GTL+ INPUT
COMPARISON OF VCC_CMOS PULL HIGH
SOCKET_370 MOBILE
IERR# N.C. 4.7K
FERR# 220 4.7K
FLUSH# 500 4.7K
PREQ# 330 4.7K
STPCLK# 410 680
INIT# 330 1K
NMI 330 4.7K
INTR 330 4.7K
IGNNE# 330 4.7K
A20M# 330 4.7K
SMI# 410 4.7K
THERMTRIP# 220 NO
PWRGOOD 330 NO
SLP# 330 10K
** SLP# IS NOT CONNECTED TO PIIX4E & PULL HIGH
10K OHM FOR QUICK START STATE (ON MOBILE MODULE)
TAP PULL HIGH OR LOW (NO USE)
SOCKET_370 MOBILE
TDI 150(UP) 1K(DOWN)
TCK 150(UP) 1K(DOWN)
TMS 150(UP) 1K(DOWN)
TRST# 680(DOWN) 1K(DOWN)
FSB FREQ SELECT PIN
BSEL1 BSEL0 FREQ
0 0 66MHz
0 1 100MHz
1 0 RESERVED
1 1 133MHz
RESERVED FOR TERMINATION DECOUPLING
RESERVED FOR 1.5V VCC_CMOS
Route GTL from BX to CPU then to these terminations
MITAC INTERNATIONAL CORP.
6120W/N - MOTHER BD
Monday, April 10, 2000